

Notice of References Cited	Application/Control No. 09/159,748		Applicant(s)/Patent Under Reexamination BARRETT, GEOFF	
	Examiner Thomas H. Stevens		Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,243,538	09-1993	Okuzawa et al.	716/5
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kimura-S., "Residue BDD and Its Application to the Verification of Arithmetic Circuits" 1995 ACM, pg.1-4.
	V	Cockett et al., "Decision Tree Reduction" 1990 ACM pg.815-842.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.